

**TRANSCIVER-PROCESSOR BUILDING
BLOCK FOR ELECTRONIC RADIO SYSTEMS**

ABSTRACT OF THE DISCLOSURE

5 A transceiver-processor building block (700) for
 implementation of radio systems, an example being used in an
 electronic radio system multifunction slice, includes several
 transceivers (704-710), a processor connected to the
 transceivers (702), and a local RF control bus (726) coupled
 between the processor (702) and the transceivers (704-710).
 10 A radio network bus (716) is also provided and connected to
 the processor (702). The local RF control bus (726) is
 inaccessible directly from outside the multifunction slice.
 In contrast, the radio network bus (716) is accessible
 directly from outside the multifunction slice to allow
 15 "stacking" of multiple slices. The building block (700) may
 also include an external control bus (728) connected to the
 processor (702). An external control bus connector (730)
 then provides direct accessibility to the external control
 bus (728) from outside the multifunction slice.